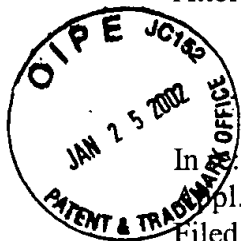


Attorney's Docket No. 41571/240701



#4
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Novotny et al.
Appl. No.: 09/990,681
Filed: November 14, 2001
For: FULLY SCALABLE COMPUTER ARCHITECTURE

Confirmation No. 2947
Group Art Unit: 2123

January 10, 2002

Commissioner for Patents
Washington, DC 20231

**INFORMATION DISCLOSURE STATEMENT
CITATION UNDER 37 C.F.R. § 1.97**

Sir:

Attached is a list of documents on form PTO-1449 together with a copy of each identified document. It is requested that the Examiner consider these documents and officially make them of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. By submitting the listed documents, Applicant in no way makes any admission as to the prior art status of the listed documents, but is instead submitting the listed documents for the sake of full disclosure.

Respectfully submitted,

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Sarah B. Simmons

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(Revised 10/2001)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number 09/990,681
Filing Date November 14, 2001
First Named Inventor Novotny et al.
Group Art Unit 2123
Examiner Name
Attorney Docket Number 41571/240701

U. S. PATENT DOCUMENTS

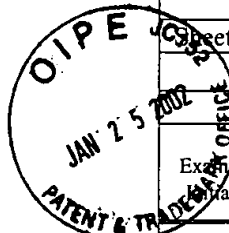
Examiner Initials*	Cite No.	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee of Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages of Relevant Figures Appear
	1	US-4,805,091	02-14-1989	Thiel et al.	
	2	US-5,603,044	02-11-1997	Annapareddy et al.	
	3	US-5,737,628	04-07-1998	Birrittella et al.	
	4	US-5,794,005	08-11-1998	Steinman	
	5	US-5,801,938	09-01-1998	Kalantery	
	6	US-5,832,272	11-03-1998	Kalantery	
	7	US-5,859,983	01-12-1999	Heller et al.	
	8	US-5,903,771	05-11-1999	Sgro et al.	
	9	US-5,920,714	07-06-1999	Schiffleger	
	10	US-6,134,514	10-17-2000	Liu et al.	
	11	US-6,138,166	10-24-2000	Heller et al.	
	12	US-6,138,167	10-24-2000	Heller et al.	
	13	US-6,230,252 B1	05-08-2001	Passint et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document Country Code - Number Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T

Examiner Signature _____ Date Considered _____

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

Complete if Known

Application Number	09/990,681
Filing Date	November 14, 2001
First Named Inventor	Novotny et al.
Group Art Unit	
Examiner Name	2123
Attorney Docket Number	41571/240701

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T
	14	DAVID M. NICOL; <i>Scalability, Locality, Partitioning and Synchronization in PDES</i> ; Pages 1-8; Department of Computer Science, Dartmouth College, Hanover, New Hampshire.	
	15	YI-BING LIN, PAUL A FISHWICK; <i>Asynchronous Parallel Discrete Event Simulation</i> ; IEEE Transactions on Systems, Man and Cybernetics; 1995; Pages 1 - 33; Vol. XX, No. Y.	
	16	ALOIS FERSCHA, SATISH K. TRIPATHI; <i>Parallel and Distributed Simulation of Discrete Event Systems</i> ; Pages 1 - 51; University of Maryland, Computer Science Department, College Park, Maryland.	
	17	G. KORNISS, Z. TOROSZKAI, M.A. NOVOTNY, P.A. RIKVOLD; <i>From Massively Parallel Algorithms and Fluctuating Time Horizons to Non-equilibrium Surface Growth</i> ; February 2000; Pages 1 - 4; arXiv:cond-mat/9909114v2 1 Feb 2000.	
	18	G. KORNISS, M.A. NOVOTNY, Z. TOROCZKAI, P.A. RIKVOLD; <i>Non-equilibrium Surface Growth and Scalability of Parallel Algorithms for Large Asynchronous Systems</i> ; 29 February 2000; Pages 1-5.	

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